

**FIG. 1**

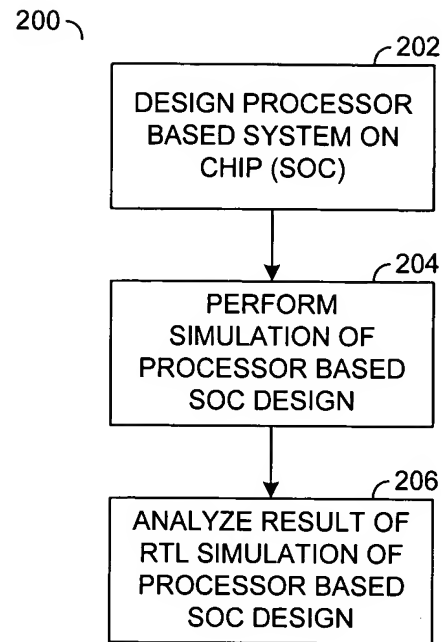
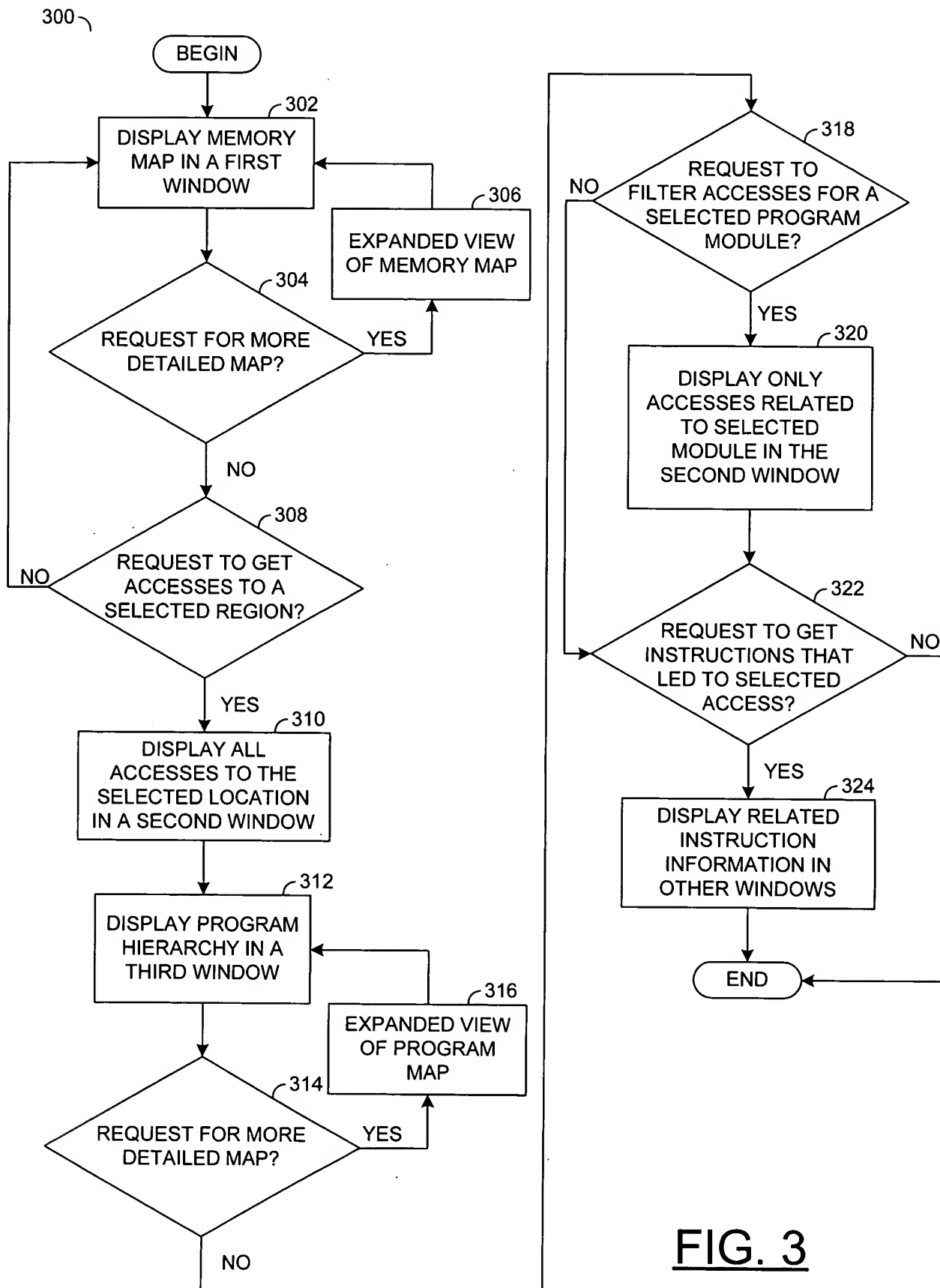
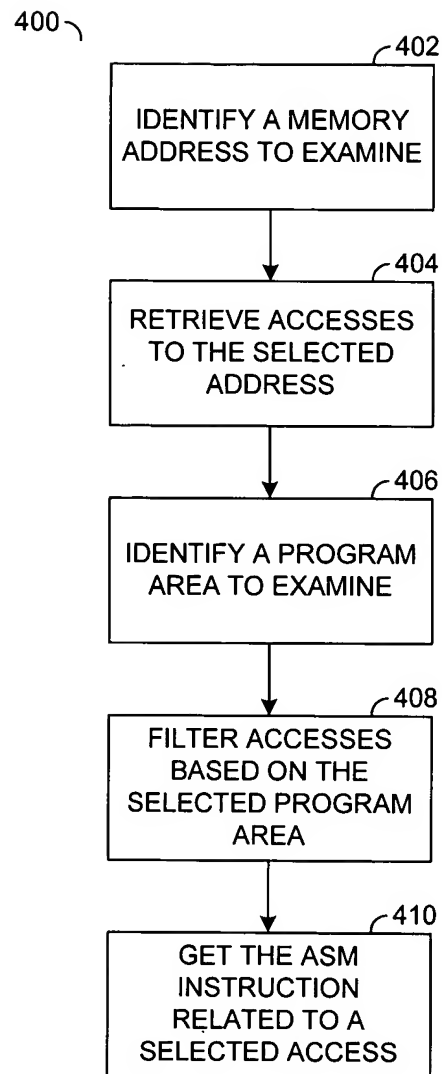


FIG. 2





**FIG. 4**

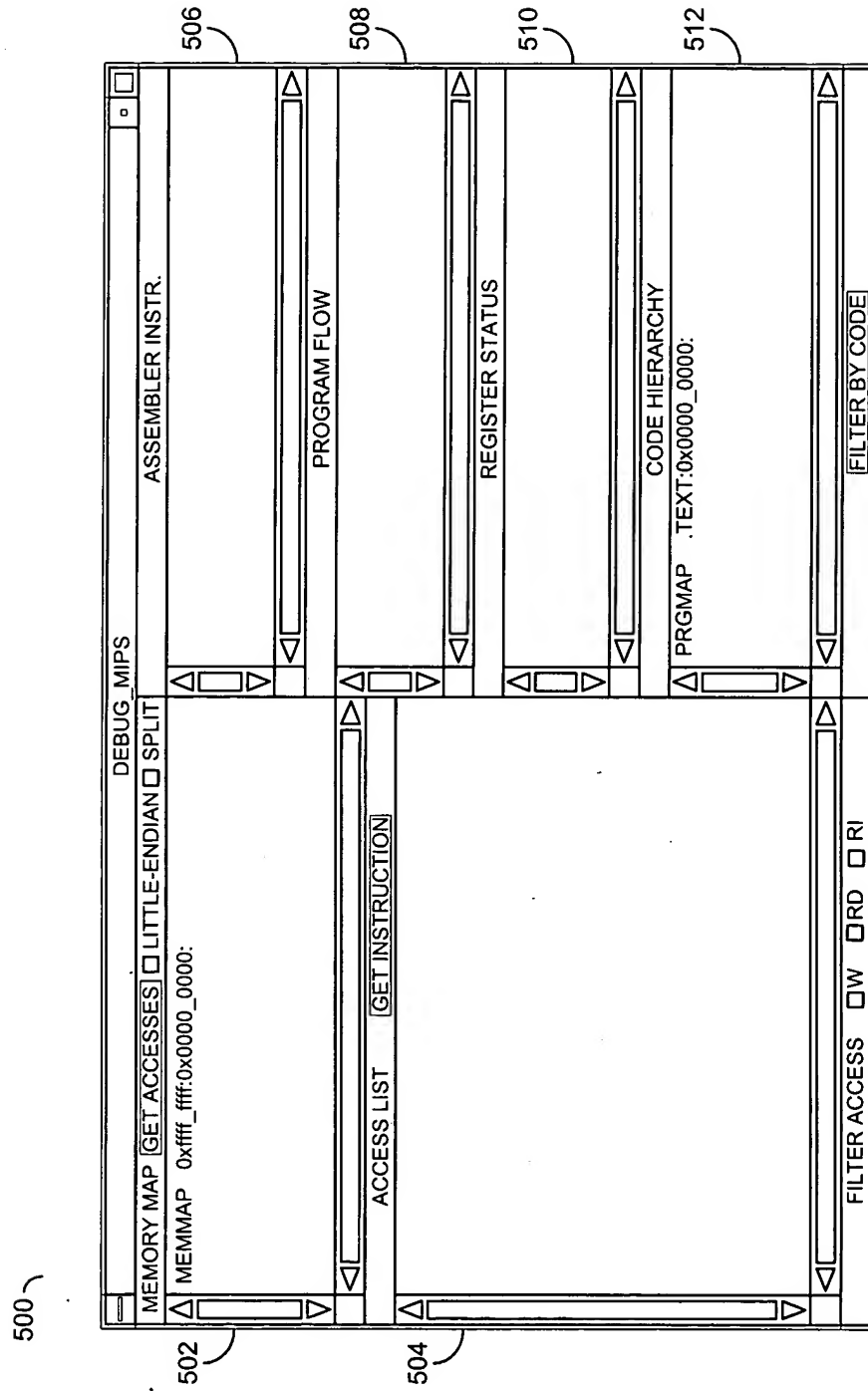
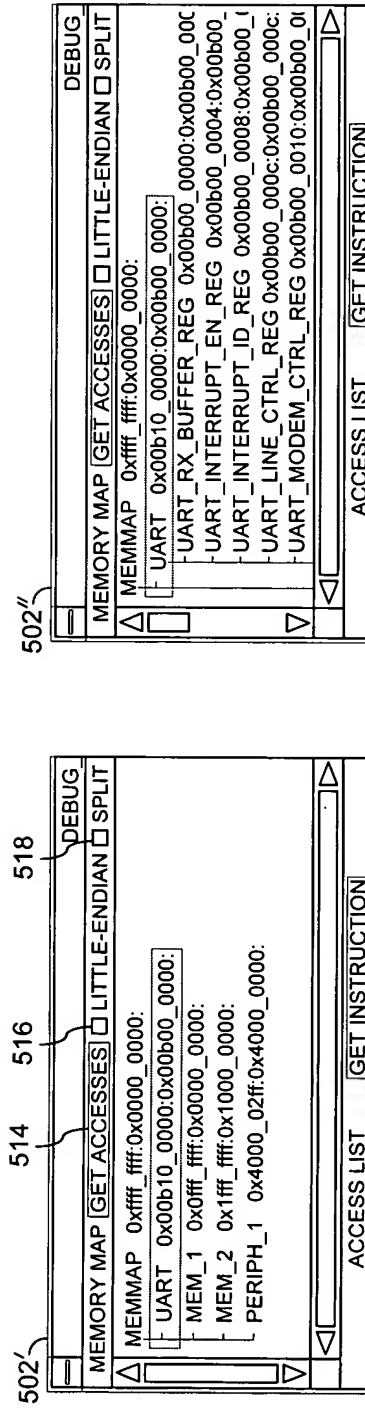
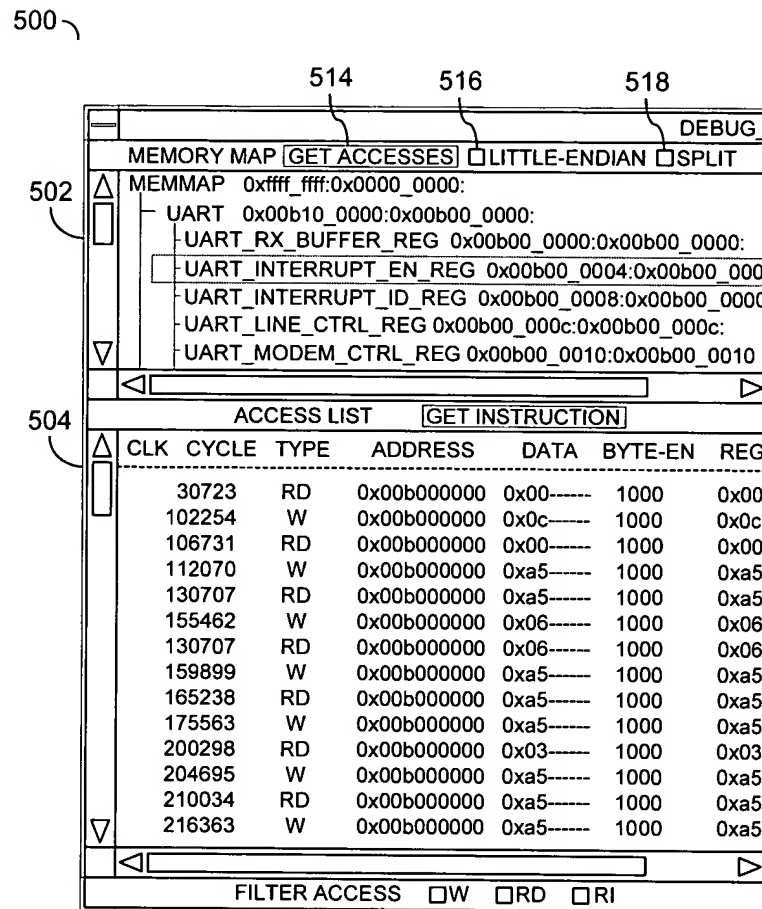


FIG. 5



**FIG. 6**



**FIG. 7**

500

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504

ACCESS LIST							GET INSTRUCTION
CLK	CYCLE	TYPE	ADDRESS	DATA	BYTE-EN	REG	
102254		W	0x00b000000	0x0c-----	1000	0x0c	
112070		W	0x00b000000	0xa5-----	1000	0xa5	
155462		W	0x00b000000	0x06-----	1000	0x06	
165238		W	0x00b000000	0xa5-----	1000	0xa5	
200298		W	0x00b000000	0x03-----	1000	0x03	
210034		W	0x00b000000	0xa5-----	1000	0xa5	
241578		W	0x00b000000	0x02-----	1000	0x02	
251314		W	0x00b000000	0xa5-----	1000	0xa5	
282778		W	0x00b000000	0x01-----	1000	0x01	
292514		W	0x00b000000	0xa5-----	1000	0xa5	
308270		W	0x00b000000	0xa5-----	1000	0xa5	
324406		W	0x00b000000	0xc5-----	1000	0xc5	
327804		W	0x00b000000	0xca-----	1000	0xca	
328972		W	0x00b000000	0xdb-----	1000	0xdb	
331080		W	0x00b000000	0xec-----	1000	0xec	
333188		W	0x00b000000	0xfd-----	1000	0xec	

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**FIG. 8**



500

520

528

504

ACCESS LIST		GET INSTRUCTION					
Δ	CLK	CYCLE	TYPE	ADDRESS	DATA	BYTE-EN	REG
	102254		W	0x00b000000	0x0c-----	1000	0x0c
	112070		W	0x00b000000	0xa5-----	1000	0xa5
	155462		W	0x00b000000	0x06-----	1000	0x06
	165238		W	0x00b000000	0xa5-----	1000	0xa5
	200298		W	0x00b000000	0x03-----	1000	0x03
	210034		W	0x00b000000	0xa5-----	1000	0xa5
	241578		W	0x00b000000	0x02-----	1000	0x02
	251314		W	0x00b000000	0xa5-----	1000	0xa5
	282778		W	0x00b000000	0x01-----	1000	0x01
	292514		W	0x00b000000	0xa5-----	1000	0xa5
	308270		W	0x00b000000	0xa5-----	1000	0xa5
	324406		W	0x00b000000	0xc5-----	1000	0xc5
	327804		W	0x00b000000	0xca-----	1000	0xca
	328972		W	0x00b000000	0xdb-----	1000	0xdb
	331080		W	0x00b000000	0xec-----	1000	0xec
	333188		W	0x00b000000	0xfd-----	1000	0xec

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522 524 526

FIG. 9

500

MIPS

ASSEMBLER INSTR.

0\_TIME\_INFO : ITAG 100565 0 22  
 1\_PRG\_ADDR : e0004098  
 2\_PHY\_ADDR : 00004098  
 3\_OP\_CODE : M32  
 4\_ASM\_CODE : ac220000 #CYCLE 854584

PROGRAM FLOW

PRGMAP (5)  
 PRGMAP : CRT0.O:\_START (5)  
 PRGMAP : LIBMIPS.A(LIBINIT.O):LIB\_INIT (5)  
 PRGMAP : LIBMIPS.A(CPU.O):WRITEIEC (5)  
 PRGMAP : LIBMIPS.A(LIBINIT.O):LIB\_INIT (5)

REGISTER STATUS

COBVA 00000000(ffffff)  
 COCAUS 90000400(ffffff)  
 COCOMP fffffff (ffffff)  
 COCONFIG 80208482 (ffffff)  
 COCONFIG1 9e994c86 (ffffff)

CODE HIERARCHY

PRGMAP .TEXT:0x0000\_0000:  
 - UNNAMED .TEXT:0x00000000e0000400:  
 - CRT0.O .TEXT:0x00000000e0000400:  
 - C\_CODE.O .TEXT:0x00000000e0000470:  
 - UARTAPI.O .TEXT:0x00000000e0001d74:  
 - UARTLOW.O .TEXT:0x00000000e00032b8:  
 - LIBMIPS.A(INTERRUPT.O) .TEXT:0x00000000e0003ba8

[FILTER BY CODE]

530

**FIG. 10**